

American Megatrends, Inc.

AMIBIOS 101094 ABSOLUTE ADDRESSES

Summary

This document describes the Absolute Address as defined in 101094 AMIBIOS which can be used by the portable code.

Revision 1.0

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American Megatrends, Inc.
6145-F, Northbelt Parkway
Norcross, GA - 30071, USA

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Revision History

Revision 1.0 WinBIOS/HiFlex 101094

0.0 INTRODUCTION

This document describes the absolute addresses in AMIBIOS which can be used by the portable code if needed.

Absolute Data Area

Address	Length	Description
F000:EE40	128bytes	Hard Disk Paramater Tables
F000:F104	BYTE	APM Connection Information
		Bit 7 Reserved
		Bit 6 0 = APM BIOS power management enabled 1 = APM BIOS power management disabled
		Bit 5 0 = APM CPU idle call stops CPU clock 1 = APM CPU idle call slows CPU clock
		Bit 4 0 = APM BIOS active version 1.0 1 = APM BIOS active version above 1.0
		Bit 3 Reserved
		Bit 2 1 = APM 32bit prot mode connection established
		Bit 1 1 = APM 16bit prot mode connection established
		Bit 0 1 = APM real mode connection established
F000:F105	BYTE	APM State Information
		Bit 7 1 = Power management functionality is enabled by APM
		Bit 6 1 = Co-operative power management between APM BIOS & APM Drive is enabled for this device (Verison 1.1 only)
		Bit 5 1 = APM Func# 0Ch currently is enabled for this device (Version 1.1 only)
		Bit 4 Reserved
		Bit 3 1 = APM controlled device (Version 1.1 only)
		Bit2-0 APM device state 000 = APM enabled state 001 = APM standby state 010 = APM suspend state 011 = APM off state 100 = Reserved 101 = Reserved 110 = Reserved 111 = APM on state
F000:F106	WORD	Pending APM Event Information
		Bit15-11 Reserved
		Bit 10 1 = System standby resume notification (Version 1.1 only)
		Bit 9 1 = User system suspend request notification (Version 1.1 only)
		Bit 8 1 = User system standby request notification (Version 1.1. only)

Bit 7	1 = Critical system suspend notification (Version 1.1 only)
Bit 6	1 = Update time notification (Version 1.1 only)
Bit 5	1 = Power status change notification (Version 1.1 only)
Bit 4	1 = Battery low notification
Bit 3	1 = Critical resume system notification
Bit 2	1 = Normal resume system notification
Bit 1	1 = System suspend request notification
Bit 0	1 = System standby request notification

F000:FEA8	DWORD	Pointer to Boot Strap CPU information array
F000:FEAC	DWORD	Pointer to FPU information array
F000:FEB0	DWORD	Pointer to Application CPU information array

Note:

1. Please see ENHIDE.DOC for details of hard disk parameter tables.
2. Please see CPU.DOC for details CPU/FPU information arrays.

Absolute Routine Address

The area F000:EEC0-F000:EF56 is reserved for absolute addresses for different routines. The idea behind this is to have a common interface to the most commonly used routines present in the system BIOS so that these routines can be used as and when needed (even from segment different from F000) without duplicating the routines.

Address	Description
F000:EEC0	Uncompress routine Input : AX:BX Source (compressed code) Segment:Offset CX:DX Destination Segment:Offset where the code will be uncompressed Output: none Register Destroyed : None Usage : Must be invoked as CALL FAR
F000:EEC3	System BIOS FIXED_DELAY routine (see ROUTINES.DOC for details) Must be invoked as CALL FAR.
F000:EEC6	Get_Processor_Info routine (see CPU.DOC for details) Must be invoked as CALL FAR.
F000:EEC9	Get_Reset_ID routine (see CPU.DOC for details) Must be invoked as CALL FAR.
F000:EECC	Get_CPU_ID routine (see CPU.DOC for details) Must be invoked as CALL FAR.
F000:EECF	Get_Vendor_Name routine (see CPU.DOC for details) Must be invoked as CALL FAR.

- F000:EED2** BIOS INT-13 Hard Disk Handler entry point
Must be invoked as INT.
- F000:EED5** Check_CMOS_Data routine (see ROUTINES.DOC for details)
Must be invoked as CALL FAR.
- F000:EED8 Reserved for future use
to
F000:EE4C
- F000:EF4D** APM 16bit protected mode entry point
Must be invoked as CALL FAR.
- F000:EF50** APM 32bit protected mode entry point
Must be invoked as CALL FAR.